

1 Mu2e Tracker Electronics - Construction of a Straw Tracker.

2 Alec Michael Lynch

3 Physics Department, University of California, Berkeley, Berkeley, CA, USA

4 Particle Physics Department, Fermilab, Batavia, IL, USA

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Abstract

The Mu2e experiment, located at the Fermi National Accelerator Laboratory (FNAL), also known as Fermilab, in Batavia, Illinois, is an in-progress particle physics experiment aiming to detect Charged Lepton Flavor Violation (CLFV) via the $\mu^- Al \rightarrow e^- Al$ interaction, a Beyond the Standard Model (BSM) process. One of the sections of the Mu2e experiment is the Tracker, which aims to identify the signal and reject the background while the experiment is running. A total of 20,736 Calibration (CAL) and High-Voltage (HV) preamplifiers (preamps) are being installed within 36 planes that make up the Tracker. During this process, MHz oscillations were discovered on the CAL preamps, obscuring desired signal detection in the $\tilde{\text{kHz}}$ range. To solve this issue, copper clips are installed onto the CALs to remove the MHz oscillations. The installation of electronics into the Tracker will be discussed in depth.

⁸ I. INTRODUCTION: GOAL OF THE RESEARCH

⁹ A. CLFV Process

¹⁰ What lies beyond the Standard Model is one of the most pressing questions in physics
¹¹ at present. The reason for this is that the Standard Model is successful in explaining
¹² most theoretical and experimental results, but fails to explain all of them. Such a process is
¹³ Charged Lepton Flavor Violation (CLFV), where one of the flavors of a lepton changes during
¹⁴ an interaction. The search for CLFV is motivated by the discovery of neutrino oscillations,
¹⁵ which were initially believed to be forbidden, meaning that lepton flavor numbers (L_e, L_μ, L_τ)
¹⁶ would be conserved [1] during a particle interaction.

¹⁷ B. Mu2e Experiment

The M2e experiment, currently under construction, aims to observe the $\mu^- + N \rightarrow e^- + N$ process, which is the primary focus of the Mu2e experiment located at the Fermi National Accelerator Laboratory (FNAL). It uses an Aluminum target, examining the $\mu^- Al \rightarrow e^- Al$ process. The project aims to observe CLFV by measuring the ratio $R_{\mu e}$, which compares the rate of neutrinoless muon-electron conversions in a nucleus's field to the rate of ordinary

muon capture on the nucleus [3].

$$R_{\mu e} = \frac{\mu^- + A(Z, N) \rightarrow e^- + A(Z, N)}{\mu^- + A(Z, N) \rightarrow \nu_\mu + A(Z - 1, N)}$$

₁₈ Mu2e intends to probe with a single-event sensitivity of $R_{\mu e} = 2.87 \times 10^{-17}$, which is four
₁₉ orders of magnitude smaller than the current limit $R_{\mu e} < 3.6 \times 10^{-20}$, established by the
₂₀ SINDRUM II experiment [3], with a 90% confidence level.

₂₁ C. Tracker

₂₂ Due to the sensitivity of this detector, a significant number of background interactions
₂₃ will be detected. Its detector, which is intended to detect the $\mu^- Al \rightarrow e^- Al$ process, also
₂₄ measures electrons with energies of 105 MeV/c[2], while accurately rejecting background
₂₅ data such as Decay in Orbit (DIO) electrons [3]. This is accomplished by using a pulsed
₂₆ beam sent through the tracker, which examines only tracks appearing between a window of
₂₇ $t = 700$ ms and $t = 1695$ ms [2]. All of this is accomplished while the Tracker lives within
₂₈ the superconducting solenoid, which is vacuumed out.

₂₉ D. Electronics Process

₃₀ This signal data is observed via straw tubes and read out by 20,736 preamplifiers
₃₁ (preamps) within 36 Tracker planes, or 18 Stations. Each station is comprised of two
₃₂ planes, and within each plane, there are six panels, each consisting of 96 preamplifiers
₃₃ designed to amplify the signal detected via the wires within the straws. In the panel, as
₃₄ shown in Fig. 1, two sections contain different preamps. The left side of the image holds
₃₅ calibration (CAL) preamps, and the right side holds High Voltage (HV) preamps. One
₃₆ of the purposes of the CAL preamp is to send pulses to the HV side to confirm proper
₃₇ communication between both sides.

₃₉ II. PLANE AND PANEL PREPARATION

₄₀ When a plane first arrives in Clean Room A, the first component that is confirmed to be
₄₁ added is the copper cooling ring. This is used to cool down the plane while the DRACs are
₄₂ being used.

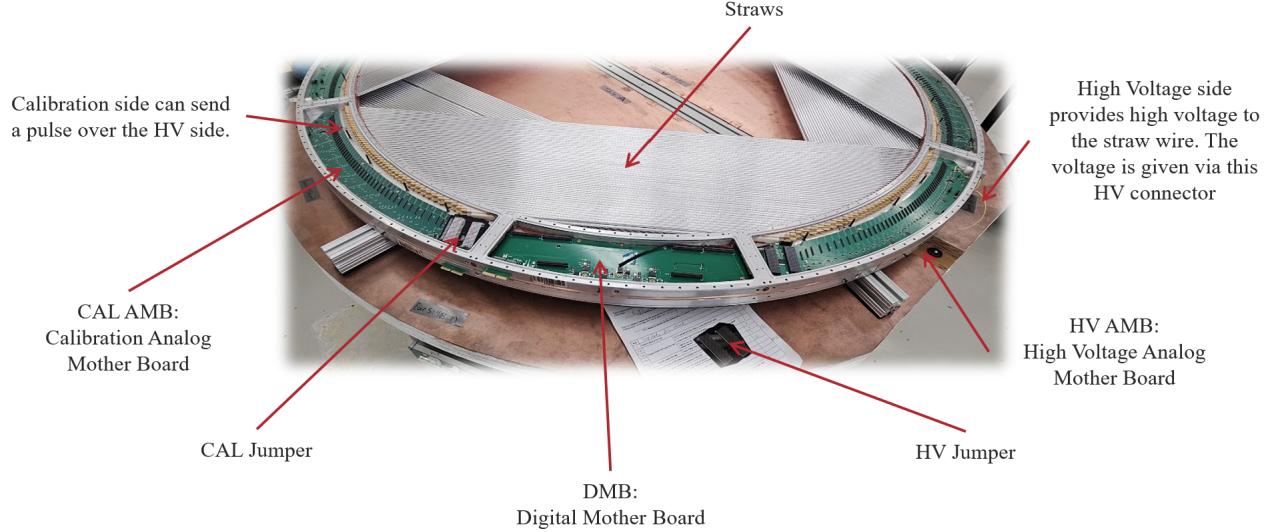


FIG. 1. Image of an empty panel. To the left is the CAL AMB, and to the right is the HV AMB.

43 **A. Cooling ring and flow test**

44 With the help of Vincent Fischer, the cooling ring is installed, and eventually, the center
 45 covers are removed from each of the three panels on the plane to access the gas tube. Before
 46 removing the side covers on each of the panels, the flow test needs to be performed. The way
 47 this is accomplished is by checking if there is at least 80% flow through the straws. ArCO₂
 48 is used for this step because it is a drier gas than air. Then, using a flow-meter, the panel is
 49 checked to see if there is 0.4 LPM coming from the wall; there should be a gas flow of 0.36
 50 LPM or higher for this to pass. If it passes, plane preparation continues. If it fails, the gas
 51 is switched from ArCO₂ to P5. P5 is a gas that is not flammable but is detectable through
 52 sniffers, and after letting the gas flow for approximately 30 minutes, the sniffer is used to
 53 detect the location of the gas. Once the leak is located, the plane team confirms what their
 54 next steps are to fix the leak.

55 **B. Flow Sensors**

56 With the test having been completed, Vincent Fischer is asked to install the flow sensors,
 57 as well as the DMB ph wire. This takes approximately one working day to cure, after which
 58 the depth of the flow sensor is measured. This is conducted to ensure the flow sensor is
 59 below the height of the frame and has sufficient space to allow gas to flow out of it.

60 **C. LVHV Component Connection**

61 When this is accomplished, the low- and high-voltage test components (power line, high-
62 voltage supply (Droege), DC converter, Keys, USB cables, and copper strips are attached.

63 The power line supplies the LV power, and the droege supplies the HV power.

64 **D. AMB preparation and installation**

65 To prepare for the installation of the Analog Mother Boards or AMBs, Mylar sheets are
66 cut and placed inside the panels, which act as a separation barrier between the aluminum
67 frame and the AMBs, to prevent shorts. Then copper washers are placed onto the screw
68 holes. These act as spacers between AMB and the mylar sheet.

69 **E. Solder Bridge Check**

70 Before the installation of the AMBs, they are checked for solder bridges. These bridges
71 cause shorts on the AMBs and cause the panel to fail during Low-voltage testing. Then
72 begin the installation of the AMBs by placing them down onto the mylar sheet within either
73 of the side sections of the panel. Next is lightly screwing the nuts onto the bolts on the
74 AMB.

75 **F. High-Voltage Preamp Installation**

76 The jumper is initially lightly placed into the DMB and AMB jump connections. This
77 is because the DMB is fixed in place, and the AMB is not, necessitating the AMB to be
78 in line with the DMB. Once the screws not under the jumper are lightly tightened, they
79 are then fully finger-tightened. Then the jumper is removed, and the screw underneath is
80 finger-tightened as well. After which, the jumper is fully inserted into the board. This is
81 repeated for the rest of the CAL and HV AMBs for each panel.

82 **G. DMB Modification/AMB HV Soldering**

83 The HV AMB voltage connector is connected to the AMB via solder so that high voltage
84 can be supplied to the panel. Bryan Wells or another trained professional is asked to modify
85 the resistors on the DMB and connect the high-voltage wire on the HV AMBs to the HV
86 socket wire.

87 **III. PREAMP INSTALLATION**

88 Initial checks are needed before installation, such as checking the travelers. These are
89 pieces of paper that record previous issues for the panel being worked on, and are checked
90 for channels that are dead. Now that the plane has been prepared, preamp installation can
91 begin.

92 The high-voltage preamps are installed first so that high-voltage continuity can be checked
93 for the wires. During installation, quality checks need to be performed, mainly on the HV
94 disconnect wire. The HV wire needs to be properly curved, similar to the Nike symbol, flush
95 with the board, no wires sticking out of the solder, clean of flux, and the solder is smooth.

96 **A. High Voltage Continuity Check**

97 After the HV preamps are plugged into the AMB, the trip setting is changed to $100\mu A$
98 and the voltage is ramped up to $\sim 100V$. Then the voltage is checked on the CAL side anode
99 pins to see if there are any issues with the wire within the straw or if any of the sockets on
100 the preamps are broken during installation.

101 **B. Calibration Preamp Installation**

102 Once the wires are checked and confirmed to be good, the CAL preamps are installed.
103 However, some channels that had previous issues, such as missing wires, missing straws,
104 anode-to-cathode shorts, or more, are marked down on our checklists. If there are channels
105 that are missing, preamps with broken sockets from installation are used.

106 **C. Low Voltage Tests**

107 *1. Setting Thresholds*

108 This test confirms proper communication between the AMB and the front-end electronics,
109 i.e., the preamps, DMB, and DRAC. By telling the system what voltage threshold to search
110 for, it attempts to create a voltage difference of 35mV, for example, between the negative
111 and positive pins on the preamp. If the script is unable to set the voltage, this is indicative of
112 potential issues. These issues range from shorts on the board, an improperly seated preamp,
113 and a solder bridge on the bottom of the header for the AMB.

114 *2. Coincidence Rates*

115 When all of the preamps have correctly set thresholds, the next Coincidence test can be
116 run. This is done via the calibration preamps sending over around 4000 pulses to the HV
117 side. This acts as a game of telephone where the CALs are attempting to verify if the HV
118 preamps can receive signals. If any preamps do not show a rate that is coincident with the
119 correct amount of pulses sent from the CAL, they are replaced with a fresh preamp.

120 With all preamps passing the threshold and coincident rate tests, the DRAC production
121 test is run, where the plots of the calpulses are affirmed to not demonstrate instances of
122 oscillations or disconnected channels.

123 **D. High Voltage Tests**

124 When the panels pass all of the LV tests, O-rings and plastic covers are attached to the
125 panel so the Plastic Cover High Voltage test can be performed. The gas input socket is
126 connected to the panel from the wall, and then turned on, where the gas flow is set to 0.5
127 LPM for each of the three panels on the plane. The flow meter on the table or wall should
128 read out \sim 1.5 LPM. As with the flow test, 15-30 minutes is how long the gas should flow
129 through the straws so that any air displaced within the volume. The voltage may be raised
130 to 500V within this time, and once 15-30 minutes have passed, ramp the voltage up by
131 250V every 15 minutes until the maximum voltage is reached. On each droege, there are
132 “different” max voltages, with the actual max voltage being 1450V. However, each droege

¹³³ has an offset so that the readout will be different for each HV supply.

¹³⁴ While ramping up the voltage, there might be sparks that can be viewed via quick spikes
¹³⁵ in the current. This may be seen either on the Droege monitor or on the Droege current
¹³⁶ meter. If there are consistent sparks above 1000nA within five-minute intervals, the panel
¹³⁷ should be troubleshooted. This may be done by using previously recorded information from
¹³⁸ the travelers or by turning off the lights and visually searching for sparks. When all of the
¹³⁹ preamps have been fixed, make sure the panel is at maximum voltage and leave it on either
¹⁴⁰ overnight or half a workday. If there are no trips during this period, the panel has passed
¹⁴¹ its plastic cover high-voltage test.

¹⁴² For the Aluminum cover, high voltage test, it essentially follows the same steps as plastic
¹⁴³ covers, albeit with aluminum covers installed. This test only needs to be run for one hour
¹⁴⁴ with no trips, as the panel has already acclimated to the voltage during its high-voltage test
¹⁴⁵ with its aluminum cover.

¹⁴⁶ E. Disconnection and purging

¹⁴⁷ If this plane is on its first half, it needs to be flipped, and the process written above
¹⁴⁸ needs to be repeated for this half. If the plane is in its second half, and all electronics are
¹⁴⁹ installed, the LV/HV components are disconnected, and the cooling ring is purged of water.
¹⁵⁰ Afterwards, it is sent to the plane for them to perform their long-term leak tests.

¹⁵¹ IV. CONCLUSION

¹⁵² In summary, from initially preparing the planes to performing the low- and high-voltage
¹⁵³ tests, there are numerous steps sensitive to minute details. In October of 2024, fifteen
¹⁵⁴ planes had their electronics installed, and currently, thirty-five of thirty-six planes have had
¹⁵⁵ their electronics installed. Each step and process for installing electronics is a result of the
¹⁵⁶ cumulative work from October 2024 to the present. It would be remiss not to acknowledge
¹⁵⁷ the massive effort from the people whose aid made the project possible, enabling it to have
¹⁵⁸ many planes with their electronics installed.

¹⁵⁹ **V. ACKNOWLEDGMENTS**

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¹⁶⁶ [1] R. E. Ray, *Mu 2 e Technical Design Report (TDR)*, Technical Design Report 15 (Fermi National
¹⁶⁷ Accelerator Laboratory, 2015) chapter 1.

¹⁶⁸ [2] R. E. Ray, *Mu 2 e Technical Design Report (TDR)*, Technical Design Report 15 (Fermi National
¹⁶⁹ Accelerator Laboratory, 2015) chapter 8.

¹⁷⁰ [3] R. E. Ray, *Mu 2 e Technical Design Report (TDR)*, Technical Design Report 15 (Fermi National
¹⁷¹ Accelerator Laboratory, 2015).