

# High time resolution, two-dimensional position sensitive MSMGRPC for high energy physics experiments

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Latest results of the R&D activity devoted to the development of the multi-strip multi-gap resistive plate counter (MSMGRPC) prototype with the highest granularity of the Time of Flight (TOF) subsystem of the Compressed Baryonic Matter (CBM) experiment are reported in this contribution. The new designed MSMGRPC fulfills simultaneously the granularity requirement and the optimum matching of the characteristic impedance of the signal transmission line to the input impedance of the front-end electronics, exploiting the special architecture of the high voltage and signal electrodes. The results in terms of the efficiency and time resolution, obtained in the laboratory cosmic ray test using a triggered data acquisition system and in heavy ion in-beam test based on a free streaming readout in mCBM setup at SIS18/GSI Darmstadt, are reported.

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## 1. Introduction

Multigap Resistive Plate Chambers (MRPCs) [1] proving a very good time resolution and high efficiency at affordable costs were implemented in the time of flight (TOF) subsystems of large scale experimental set-ups, i.e HADES [2], FOPI [3], STAR [4], ALICE [5]. However, being based on resistive electrodes made of commercial float glass (with a resistivity of the order of  $10^{12} - 10^{13} \Omega\text{cm}$ ), their rate capability does not exceed  $\sim 1 \text{ kHz/cm}^2$ .

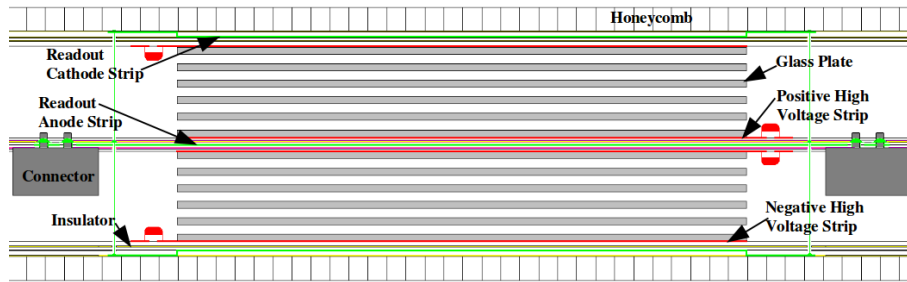
The next generation experiments, as is Compressed Baryonic Matter (CBM) experiment at the future experimental Facility for Antiprotons and Ion Research (FAIR) in Darmstadt, Germany, aims to investigate the QCD phase diagram in the region of high net-baryon densities and moderate temperatures, at interaction rates up to  $10^7$  interaction/s [6]. In order to cope with such very high interaction rates, CBM will be equipped with fast and radiation hard detectors operated by a self-triggered readout electronics. The signals will be processed by a free-streaming data acquisition system, whereby all signals passing the electronic threshold will be digitized, time stamped and processed. The experimental setup includes a Time of Flight (TOF) wall [7] based on MRPCs with multi-strip readout [8] for hadron identification in fixed-target heavy ion collisions at SIS100 energies, up to 14A GeV. The challenge for the TOF subsystem is to keep an efficiency larger than 95% and a system time resolution better than 80 ps at high particle flux (up to  $30 \text{ kHz/cm}^2$ ) and hit multiplicity (up to 1000 tracks/event). For occupancy value below 5%, the effective cell size of the TOF wall is scaled with the track density (which drops from low to large polar angles).

Therefore, we have focused on an intense R&D activity [9, 10, 11] for development of a Multi-Gap Resistive Plate Chamber with differential Multi-Strip readout (MSMGRPC) for the most demanding region of the TOF wall, the region of low polar angles ( $2.5^\circ - 11^\circ$ ) called inner zone. We report in this paper latest results of the activity devoted to develop a MSMGRPC prototype with the highest granularity of the CBM-TOF wall. The counter fulfills simultaneously the granularity requirement and the optimum matching of the characteristic impedance of the signal transmission line impedance to the input impedance of the front-end electronics.

## 2. MSMGRPC prototype with the highest granularity of the CBM-TOF wall

### 2.1 Inner geometry

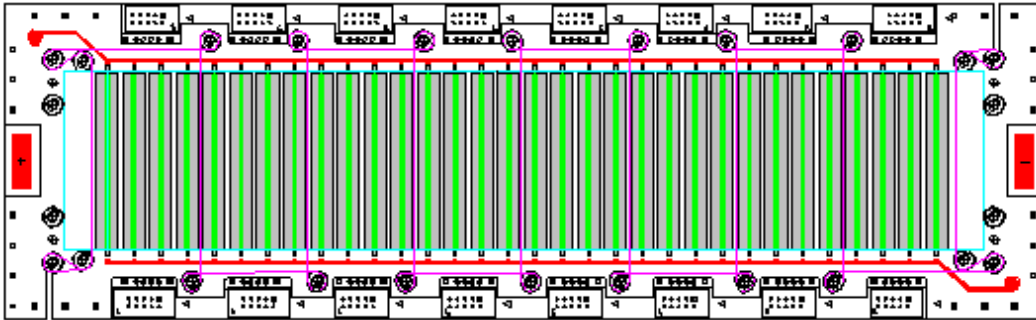
The MSMGRPC inner geometry is presented in Fig. 1. The typical architecture of our prototypes is a double stack structure, the six resistive glass plates of each stack being separated by five gaps of  $140 \mu\text{m}$  size. In order to cope with the high particle flux (see Sect. 1), the resistive electrodes are made of a special, new developed low resistivity glass [12], with an average resistivity of  $1.5 \times 10^{10} \Omega\text{cm}$ . The outermost glass plates of each stack are in contact with the Cu strips of the high voltage electrodes for both positive and negative polarities. The overlaying readout electrodes are electrically isolated by the high voltage electrodes by thin insulator layers in order to prevent any possible discharges between the high voltage and readout electrodes. Two honeycomb plates positioned on the outer sides of the counter keep the alignment of the electrodes and the mechanical stability of the detector. The high voltage (HV) is distributed by a common high voltage line to each individual strip through 12 k $\Omega$  resistors which keep localized any possible perturbation of the potential of high voltage strips.



**Figure 1:** Cross section along the strip (green - readout strips, red - HV strips) .

## 2.2 Readout electrodes and transmission line impedance

The counter architecture has a strip structure for both HV and readout electrodes, with the same pitch for both of them (however, their strip width (HV and readout) can differ). The anode and cathode strips overlapped in space together with the layers positioned between them define a signal transmission line.



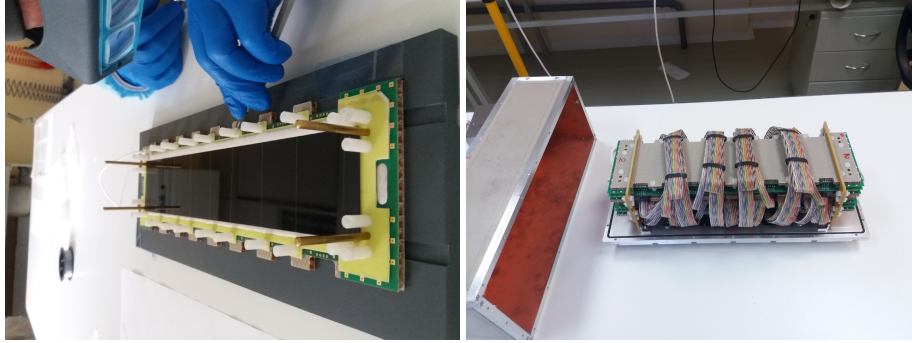
**Figure 2:** Front view of the prototype: green - readout strips, gray - HV strips, red - common HV line.

The 9.02 mm pitch and 60 mm length of the strips of the readout and high voltage electrodes have been chosen based on the maximum length (300 mm) of the available low resistivity glass and the occupancy requirement. The 7.37 mm high voltage strip width defines the granularity, taking into account the expected cluster size and the adjusted strip length. The width of the readout strip was estimated based on an innovative method described in reference [13]. For a given strip width of the HV electrodes, one could adjust the readout strip width in the signal propagation simulation using the software package APLAC [14], such to get a desired impedance which matches the input impedance of the front-end electronics. The values of the thickness and permittivity of the successive layers positioned between overlapped anode and cathode strips are considered in the simulations. A 1.27 mm readout strip width leads to a transmission line impedance of 97  $\Omega$ , which matches to the input impedance of the the front-end electronics channel. The alignment and overlap of the readout (green) and high voltage (gray) strips can be seen in the front view cross section shown in Fig. 2. On the up and down lateral sides are shown the high voltage lines (red) for positive and negative polarity. The signal connectors (black) are soldered on the lateral sides of the anode readout plate. The 300 mm maximum size of the low resistivity glass covered by 32 strips defines the length of the counter. The signals are readout in a differential mode, both the anode and

the cathode readout strip signals being fed into the input of a front-end electronic channel. Each signal transmission line is readout at both ends.

### 2.3 Prototype assembling

Two identical prototypes were assembled (Fig. 3-left) and mounted one on top of the other in



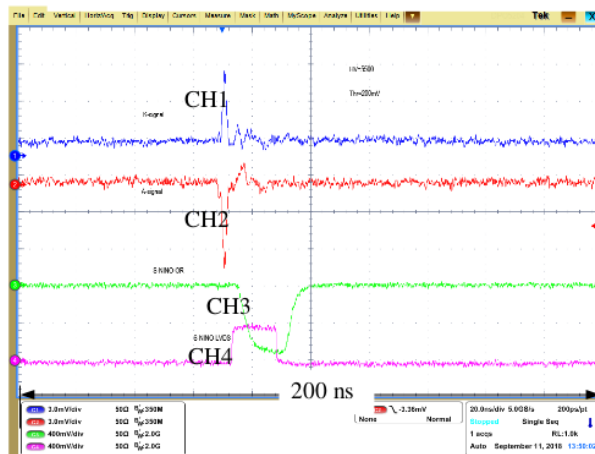
**Figure 3:** Spacer stretching across the resistive electrode - left. View of the assembled prototypes - right.

a gas tight and electromagnetically screened box, (Fig. 3 - right).

## 3. Cosmic rays and in-beam tests

### 3.1 Cosmic rays tests

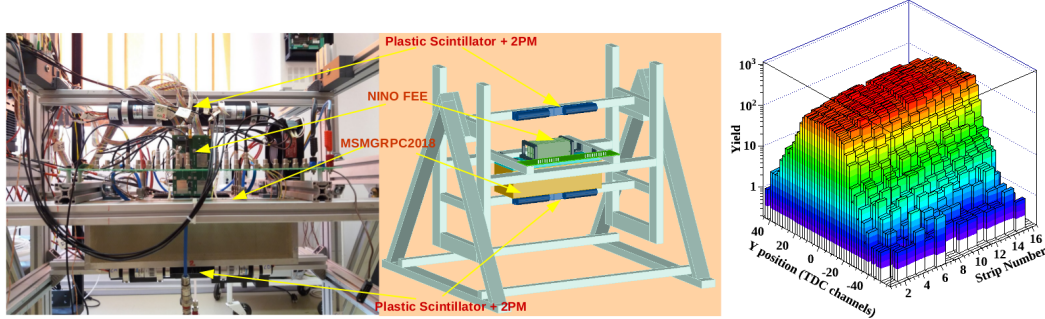
The assembled counters were first tested in the detector laboratory using a gas mixture of 90% $C_2H_2F_4$ +10% $SF_6$  and an electric field of 157 kV/cm ( $HV = \pm 5.5$  kV).



**Figure 4:** Cathode (blue - CH1) and anode (red - CH2) picked-up signals from one side of a transmission line, recorded directly on the oscilloscope, without any amplification. The logic OR signal (green - CH3) of the FEE card and the LVDS output of the NINO channel (magenta - CH4) are displayed.

The typical analog anode (red) and cathode (blue) signals recorded directly on Tektronix TDS 7254B 3 GHz oscilloscope from one end of a signal transmission line are shown in Fig. 4, the other

end being fed into the input of one of the 8 channels of NINO [15] based discriminator/amplifier, used as front-end electronics (FEE). As one can be seen, on a large time scale of 200 ns any reflection is displayed. On the same screenshot from Fig. 4, the logic OR signal (green) of the FEE card and the LVDS output of the NINO channel (magenta) were recorded.

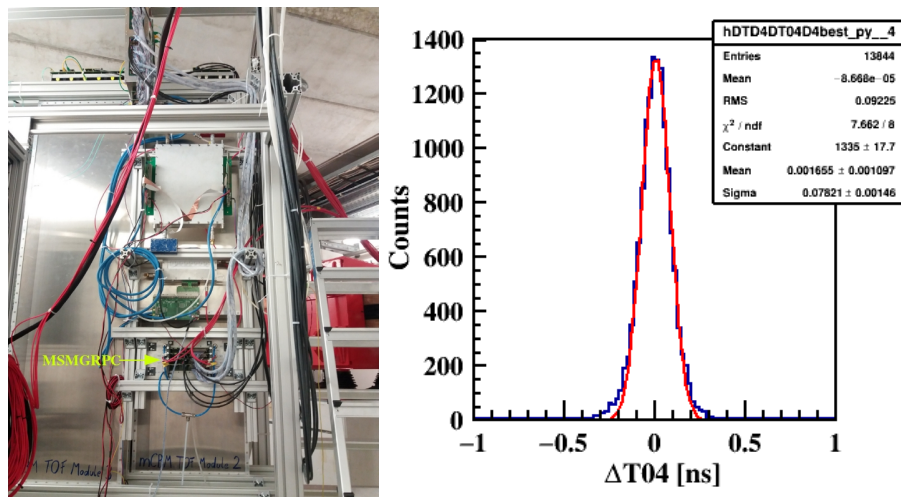


**Figure 5:** The cosmic ray experimental setup (left). 3D plot of the position of the cosmic ray events on the 16 measured strips (right).

In the experimental setup shown in Fig. 5 - left side, the two counters were sandwiched between two plastic scintillator bars of 10 cm length and 1.5 cm x 1.5 cm cross section. The aim was to estimate the detector efficiency and time resolution before testing them in-beam, in real operation conditions. A triggered data acquisition (DAQ) system equipped with CAEN TDCs was used for signal processing. A 3D view of the cosmic-ray illumination across 16 operated strips per counter is shown in Fig. 5. For a 160 mV FEE threshold the estimated efficiency was of 95% with an average cluster size of 1.8 strips. A single counter time resolution of  $64 \pm 9$  ps was obtained after slewing corrections.

### 3.2 In-beam tests

The prototypes were tested in-beam in a dedicated CBM test setup called mCBM at the SIS18



**Figure 6:** Position in the mCBM experimental setup (left). Calibrated time difference spectrum (right).

accelerator of GSI Darmstadt, with reaction products produced by 1.6A GeV <sup>107</sup>Ag beam incident

on a Au target. The above mentioned prototypes were positioned in the experimental setup downstream relative to the counters developed for the outer zones of the TOF wall (Fig. 6). A diamond detector, positioned in front of the target was used as beam reference. The signals delivered by the detectors were fed into PADIX [16] front-end electronics and processed by a free streaming DAQ equipped with GET4-TDCs [17]. For the estimation of the time resolution one of the two above described prototypes was considered as reference (REF) and the other one as detector under study (DUT). The obtained system time resolution (includes the contribution of both REF and DUT counters) was of 78 ps. If an equal contribution of the two identical counters to the time resolution is supposed, a single counter time resolution of 55 ps (including electronics chain contribution) is estimated. The multi-dimensional data analysis is on-going and further results will follow.

#### 4. Conclusions

A prototype of the counters with the highest granularity of the CBM-TOF wall was designed with the signal transmission line impedance matched to the input impedance of the front-end electronics. The cosmic ray test showed that the counter is free of reflections and has a very good performance in terms of efficiency and time resolution. The performance of the counter is confirmed by the preliminary results obtained in close to real conditions operation in the mCBM setup installed at SIS18, GSI Darmstadt.

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